

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 1, line 24 to read:

An objective of the invention herein is to provide a basic input/output system updating method that divides a memory in an electronic device into a system program area and a boot program area, wherein the basic ~~input/output~~ input/output system (BIOS) program is stored in the ~~said~~ system program area and an initialization sequence is stored in the boot program area. When the initialization sequence in the ~~said~~ system program area is destroyed and the BIOS program is lost, user is able to toggle a switch to a closed position to connect the same microprocessor to the boot program area and read the initialization sequence stored in the boot program area. As such, the ~~said~~ microprocessor, during the initialization sequence, activates ~~[[a]]~~ memory components connected with the ~~said~~ electronic device, reads a BIOS program stored in the ~~said~~ memory components, and records it into the system program area, thereby circumventing the failure of the ~~said~~ electronic device to initialize because the BIOS program was lost from the ~~said~~ system program area.

Please amend the paragraph beginning on page 2, line 13 to read:

Another objective of the invention herein is to provide a basic input/output system updating method that divides the memory in an electronic device into a system program area and a boot program area, wherein the BIOS program is stored in the ~~said~~ system program area and the initialization sequence is stored in the boot program area such that when the ~~said~~ system program area is destroyed and the BIOS program is lost, the user toggles a switch to the close deposition to connect the ~~said~~ microprocessor to the ~~said~~ boot program area to thereby enable the connection of the ~~said~~ microprocessor to the boot program area and its reading of the initialization sequence in the boot program area. As such, during the initialization sequence, the ~~said~~ microprocessor, during the

initialization sequence, activates [[a]] memory components connected with the ~~said~~ electronic device, reads a BIOS program stored in the ~~said~~ memory components, and records it into the system program area, thereby circumventing the failure of the ~~said~~ electronic device to initialize because the BIOS program was lost from the ~~said~~ system program area.

Please amend the paragraph beginning on page 4, line 15 to read:

In the ~~said~~ embodiment, referring to FIG. 1, the memory 11 of the invention herein is a flash ROM and the ~~said~~ switch SW1 is a manually operated component that enables the user to toggle the ~~said~~ switch SW1 into a closed state (ON) or an open state (OFF); when the user switches on the power supply of the ~~said~~ electronic device 1, the ~~said~~ microprocessor 13 issues an address signal (such as FFFFO) via a decoder which, following decoding, is sent to the ~~said~~ system program area and boot program area 111 and 112, thereby enabling the ~~said~~ microprocessor 13 to respectively assign the starting position of the ~~said~~ system program area and boot program area 111 and 112 such that when the user toggles the ~~said~~ switch SW1 to the ON state and brings the ~~said~~ microprocessor 13 into a connection with the ~~said~~ boot program area 112, the ~~said~~ microprocessor 13 reads from the ~~said~~ boot program area 112 and writes into the ~~said~~ boot program area 112; or, when the user toggles the ~~said~~ switch SW1 to the OFF state and thereby establishes a connection between the ~~said~~ microprocessor 13 and the ~~said~~ system program area 111, the ~~said~~ the ~~said~~ microprocessor 13 reads from the ~~said~~ system program area 111 and writes into the ~~said~~ system program area 111.

Please amend the paragraph beginning on page 5, line 5 to read:

In another most preferred embodiment of the invention herein.. referring to FIG. 1, the ~~said~~ microprocessor 13 links with a local area network (LAN) during the ~~said~~

initialization sequence, downloads a BIOS program from the ~~said~~ LAN, and records the ~~said~~ BIOS downloaded from the ~~said~~ LAN into the ~~said~~ system program area 111.

Please amend the paragraph beginning on page 5, line 9 to read:

In the ~~said~~ embodiment, referring to FIG. 1, the memory 11 of the invention herein is flash ROM and the ~~said~~ switch SW1 is a manually operated component that enables the user to toggle the ~~said~~ switch SW1 into a closed state (ON) or an open state (OFF); when the user switches on the power supply of the ~~said~~ electronic device 1, the ~~said~~ microprocessor 13 issues an address signal (such as FFFFO) via a decoder which, following decoding, is sent to the ~~said~~ system program area and boot program area 111 and 112, thereby enabling the ~~said~~ microprocessor 13 to respectively assign the starting position of the ~~said~~ system program area and boot program area 111 and 112 such that when the user toggles the ~~said~~ switch SW1 to the ON state and brings the ~~said~~ microprocessor 13 into a connection with the ~~said~~ boot program area 112, the ~~said~~ microprocessor 13 reads from the ~~said~~ boot program area 112 and writes into the ~~said~~ boot program area 112; or, when the user toggles the ~~said~~ switch SW1 to the OFF state and thereby establishes a connection between the ~~said~~ microprocessor 13 and the ~~said~~ system program area 111, the ~~said~~ the ~~said~~ microprocessor 13 reads from the ~~said~~ system program area 111 and writes into the ~~said~~ system program area 111; in addition, the computer manufacturer can down-load the ~~said~~ BIOS through the ~~said~~ LAN.

Please amend the paragraph beginning on page 5, line 25 to read:

Referring to FIG. 1 and FIG. 2, when the user switches on the ~~said~~ electronic device 1, the ~~said~~ microprocessor 13 executes the following procedures:

Please amend the paragraph beginning on page 6, line 2 to read:

At step 101, a determination is made as to whether the ~~said~~ switch SW1 is in the ON state. If the result is affirmative, the next procedure is executed, if the result is negative, then the BIOS program in the system program area 111 of the ~~said~~ memory 11 is read to execute the normal initialization routine.

Please amend the paragraph beginning on page 6, line 6 to read:

At step 102, the boot program area 112 in the ~~said~~ memory 11 is read to preset the initialization sequence.

Please amend the paragraph beginning on page 6, line 8 to read:

At step 103, the ~~said~~ initialization sequence activates the hard disk drive operation of the ~~said~~ electronic device 1.

Please amend the paragraph beginning on page 6, line 10 to read:

At step 104, a determination is made as to whether a BIOS program is stored in the ~~said~~ hard disk drive. If the result is affirmative, the ~~said~~ BIOS program is read and recorded in the system program area 111 of the ~~said~~ memory 11, which concludes the procedure, otherwise, the sequence proceeds to the next step.

Please amend the paragraph beginning on page 6, line 14 to read:

At step 105, the ~~said~~ initialization sequence activates the IDE interface operation of the ~~said~~ electronic device 1.

Please amend the paragraph beginning on page 6, line 16 to read:

At step 106, a determination is made as to whether a BIOS program is present in the IDE interface connected to the hard disk drive. If the result is affirmative, the ~~said~~ BIOS program is read and recorded in the system program area 111 of the ~~said~~ memory 11, which concludes the procedure; otherwise, the sequence proceeds to the next step.

Please amend the paragraph beginning on page 6, line 20 to read:

At step 107, the ~~said~~ initialization sequence activates the LAN.

Please amend the paragraph beginning on page 6, line 21 to read:

At step 108, linked to the LAN, the BIOS program of the ~~said~~ LAN is downloaded and recorded in the system program area 111 of the ~~said~~ memory 11, which concludes the sequence.

Please amend the paragraph beginning on page 6, line 24 to read:

As such, when the BIOS program is lost from the system program area 111 in the memory 11 of the ~~said~~ electronic device 1, the ~~said~~ microprocessor 13 can read the ~~said~~ BIOS program in the ~~said~~ system program area 111 to execute the normal initialization sequence; when the BIOS program of the ~~said~~ system program area 111 is destroyed by a computer virus or a user-induced BIOS reconfiguration failure such that the BIOS program is lost from the ~~said~~ system program area 111, the ~~said~~ microprocessor 13 is capable of reading the initialization sequence from the ~~said~~ boot program area 112 to activate the hard disk drive, the IDE interface, and the LAN as well as record the BIOS program previously stored therein to the ~~said~~ system program area 111, thereby circumventing the failure of initializing the ~~said~~ electronic device 1 because the BIOS program was lost from the ~~said~~ system program area 111.